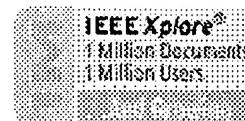




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Solid-State Circuits, IEEE Journal of , Volume: 34 , Issue: 11 , Nov. 1999  
Pages:1557 - 1563

[Abstract] [PDF Full-Text (340 KB)] IEEE JNL

**2 High-density chain ferroelectric random access memory (chain FRAM)**

*Takashima, D.; Kunishima, I.;*  
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[Abstract] [PDF Full-Text (140 KB)] IEEE JNL

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[Abstract] [PDF Full-Text (300 KB)] IEEE CNF

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*Takashima, D.; Oowaki, Y.; Kunishima, I.;*  
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